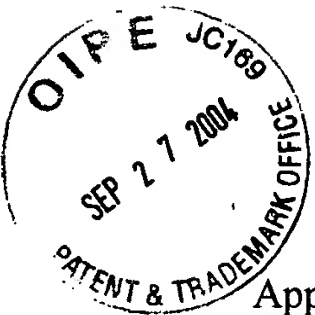


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Toshimitsu Taniguchi et al. Art Unit : 2825
Serial No. : 09/891,580 Examiner : Igwe U. Anya
Filed : June 26, 2001
Title : SEMICONDUCTOR DEVICE MANUFACTURING METHOD

MAIL STOP AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO ACTION OF JUNE 23, 2004

Please amend the above-identified application as follows:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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